



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of

AKAGAWA et al.

Atty. Ref.: 1035-481

Serial No. 10/705,306

Group: Unknown

Filed: November 12, 2003

Examiner: Unknown

For: SEMICONDUCTOR DEVICE AND FABRICATION PROCESS  
THEREOF

\* \* \* \* \*

December 5, 2003

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Supplementing the Information Disclosure Statement filed November 12, 2003 which contained an error in the form PTO-1449 in the date of U.S. 6,538,326. Attached is a corrected form PTO-1449 listing the correct date of 3/2003. The Examiner is requested to initial the attached form PTO-1449 and to return a copy of the initialed document to the undersigned as an indication that the reference has been considered and made of record.

Respectfully submitted,

**NIXON & VANDERHYE P.C.**

By:

H. Warren Burnam, Jr.

Reg. No. 29,366

HWB:pc  
1100 North Glebe Road, 8th Floor  
Arlington, VA 22201-4714  
Telephone: (703) 816-4000  
Facsimile: (703) 816-4100

## INFORMATION DISCLOSURE

ATTY. DOCKET NO.

SERIAL NO.

1035-481

10/705,306

APPLICANT

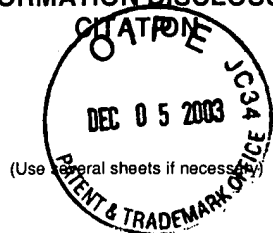
AKAGAWA et al.

FILING DATE

GROUP

November 12, 2003

Unknown



## U.S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

[illegible]

**OTHER DOCUMENTS** (including Author, Title, Date, Pertinent pages, etc.)

[illegible]

\*Examiner

Date Considered

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application.